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FACSIMILE COVER LETTER

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NAME: Commissioner of Patents – JOSEPH CHANG
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RE: United States Patent Application no: 10/796,419
Gerald Harron et al
AMPLITUDE & PHASE MODULATION USING DUAL DIGITAL DELAY VECTORS
EXAMINER – Joseph Chang - GROUP 2817

Response/amendment to Notice of Action dated July 13, 2005, replacement drawing of Figure 1, copy of original drawings (4 sheets) and a Terminal Disclaimer.

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EXAMINER:	Joseph Chang	JUL 28 2005
GROUP:	2817	
APPLICANT:	Gerald Harron et al	
SERIAL NO:	10/796,419	
FILED:	March 10, 2004	
FOR:	AMPLITUDE AND PHASE MODULATION USING DUAL DIGITAL DELAY VECTORS	

U.S. Patent and Trademark Office
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U.S.A.

Dear Sir:

Responsive to the Notice of Action dated July 13, 2005.